arm

CEO Keynote

Cautionary Note Regarding Forward-Looking Statements

This presentation contains forward-looking statements that involve a number of risks and uncertainties. Arm Holdings plc (the "Company" or "Arm") cautions readers that any forward-looking information is not a guarantee of future performance and actual results could differ materially from the information expressed or implied by these forward-looking statements. When used in this presentation, words such as "may," "might," "will," "could," "would," "should," "expect," "is/are likely to," "intend," "plan," "objective," "anticipate," "believe," "estimate," "predict," "potential," "target," "continue," "ongoing" and similar expressions and any other statements that are not historical facts are intended to identify forward-looking statements.

Such forward-looking statements include, but are not limited to, projections and estimates of the TAM for our products and our expectations regarding revenue, licensing and royalty mix and growth, in both the near and long-term; our expectations regarding the impact of the introduction of new products on our existing operations, customer base, and demand; our vision for the future of Arm and AI computing; our ability to implement new products and business initiatives, including the expansion of our business model into production silicon; Arm AGI CPU and its expected performance, scale, efficiency and projected energy savings; our annual product roadmap; data center and agentic AI growth generally, including anticipated data center capacity; the Company's partnerships and customer expectations; projections relating to our future financial results, growth, products and services; our financial position; our market opportunity, demand and growth drivers; and any other statements that are not historical facts.

Forward-looking statements involve a number of risks, uncertainties or other factors beyond our control that may cause actual results to differ materially. These factors include, but are not limited to, our ability to implement our strategic initiatives; our development of new products and technologies; our entry into new business areas, including production silicon, and the associated execution risks; our reliance on third parties to manufacture, assemble, package and test our products; market acceptance of our products; the accuracy of comparative performance benchmarks and claims; the impact of technological development and competition; the development and growth of the AI market generally; any potential design, manufacturing, hardware or software defects; changes in customer preferences and demands; changes in industry standards; global economic, political and market conditions and fluctuations; geopolitical instability, government and industry regulation; and global competition. For a complete discussion of factors that could materially affect our financial results and operations, please refer to the reports we file from time to time with the SEC, including our Annual Report on Form 20-F. Copies of reports we file with the SEC are posted on our website and are available without charge. The Company undertakes no obligation to publicly update any forward-looking statement, whether as a result of new information, future events or otherwise.

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The AI engine

~117 Billion

~117 Billion

Total humans to have ever lived

350+ Billion

350+ Billion

Total Arm chips to have ever shipped

350+ Billion

Total Arm chips to have ever shipped

3X

Total humans ever

Based on estimates.

350+ Billion

Total Arm chips to have ever shipped

7X

Non-Arm CPUs combined

Based on estimates.

350+ Billion

Total Arm chips to have ever shipped

160

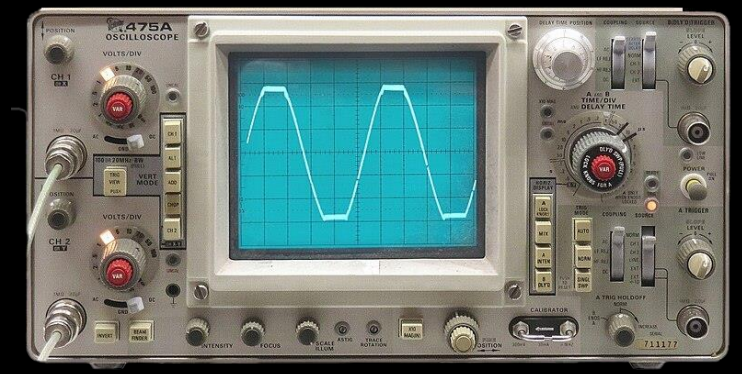
Arm chips per global household

Based on estimates.

Born to run off batteries

Born to run off batteries

Born to run off batteries



Started a revolution of smartphones

Expanded to the platform leader across verticals

Ecosystem of ecosystems

22M+ software developers

YouTube zoom Uber box

workday Snap Inc.

Spotify UNREAL ENGINE

Raspberry Pi Qt The Qt Project LiteRT

ExecuTorch Meta XNNPACK paloalto NETWORKS

LLaMA elastic Redis ORACLE

salesforce snowflake Hugging Face

PyTorch ONNX RUNTIME docker

Google Cloud kubernetes

ATLASSIAN aws SAP

Microsoft Azure databricks

ubuntu Red Hat SUSE

GAZEBO pony.ai Apex.AI

ROS Panasonic foretellix

mapbox Applied Intuition

here tomtom TIER IV

alexa cerence Dolby

TESLA WAYMO Aurora

XPENG RENESAS

NXP THE AUTOWARE FOUNDATION ZOOX

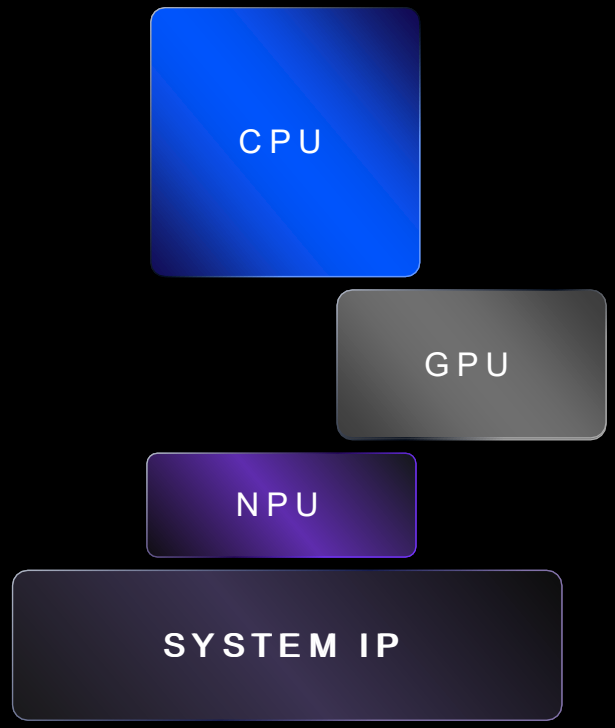
iOS Windows Android macOS

OpenAI ANTHROP\C

QNX Android Auto

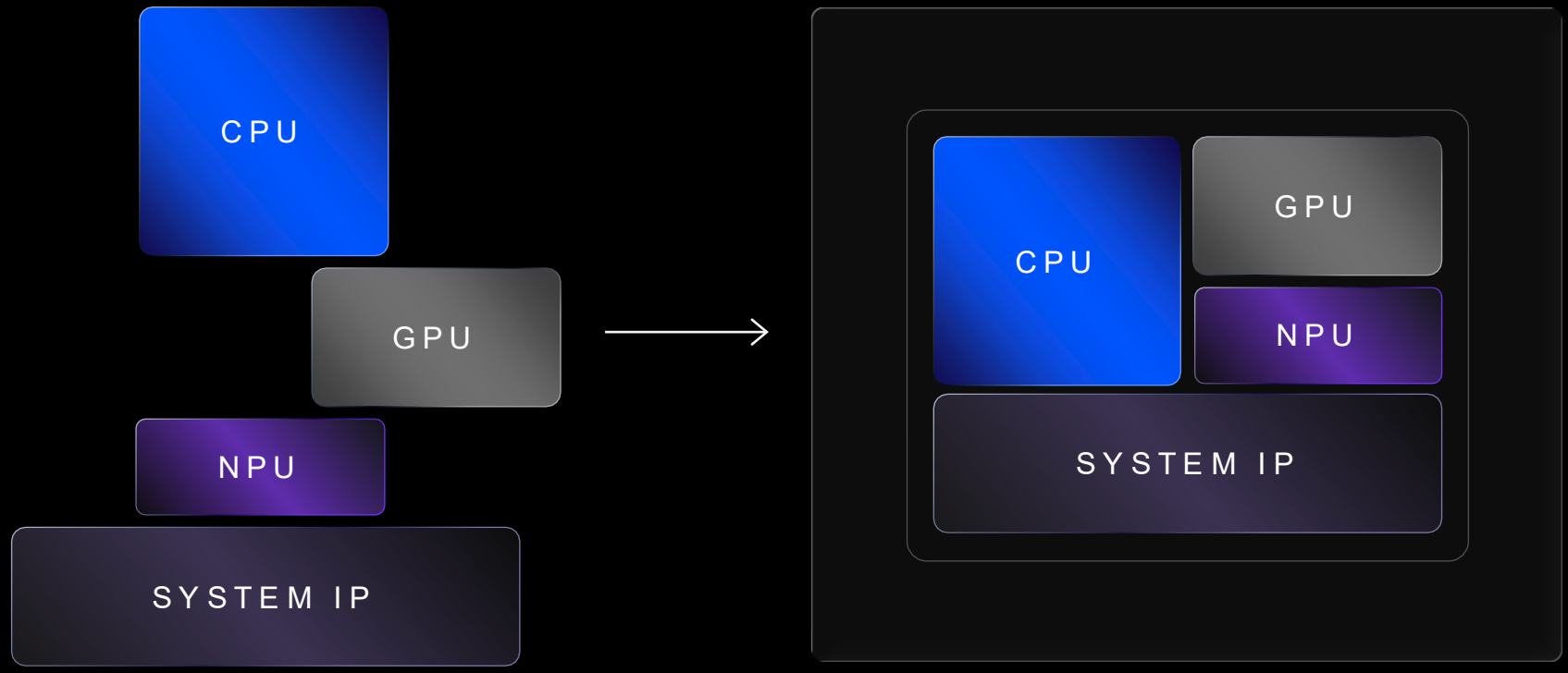
Our evolution

Our evolution



IP

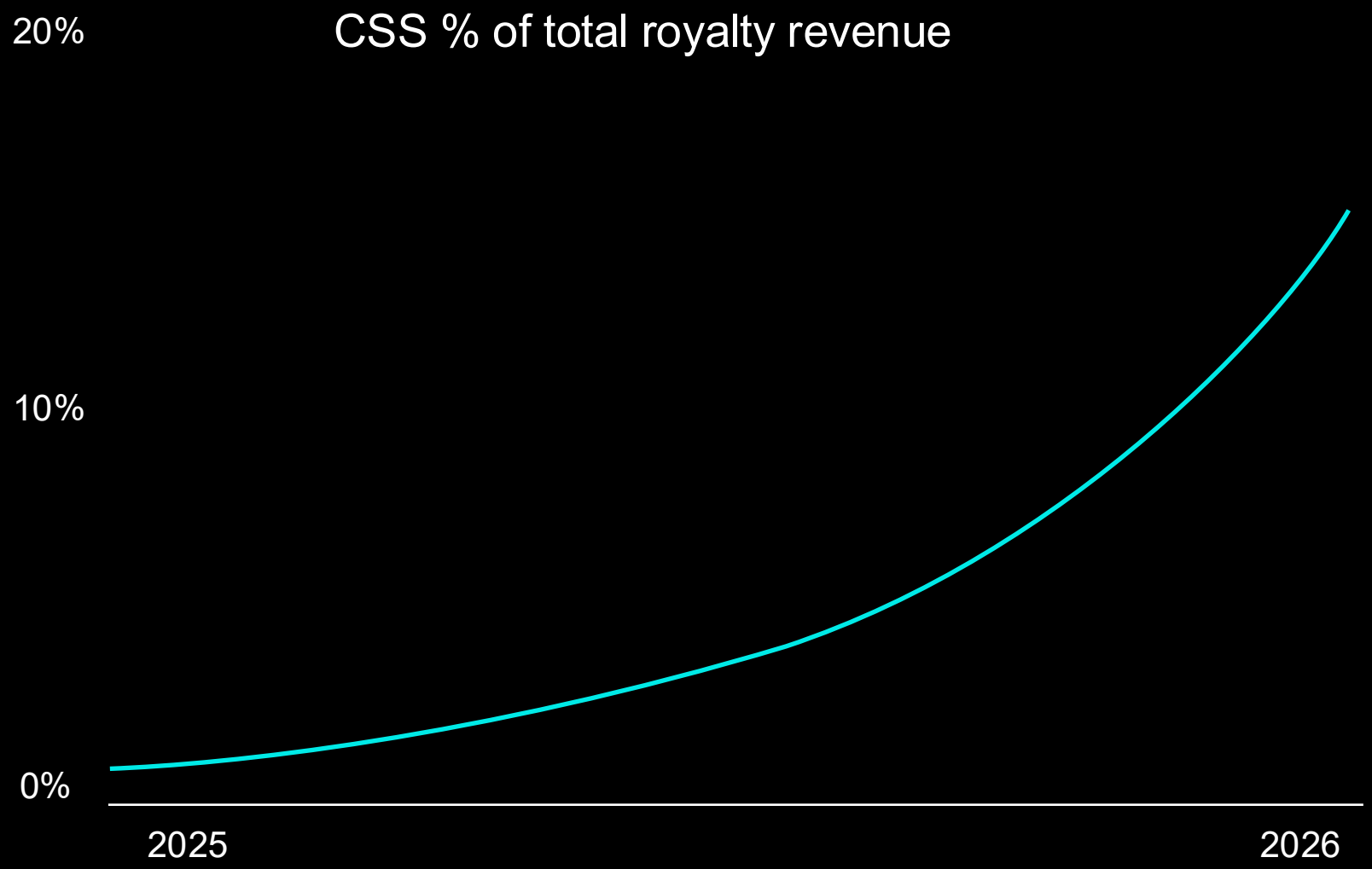
Our evolution



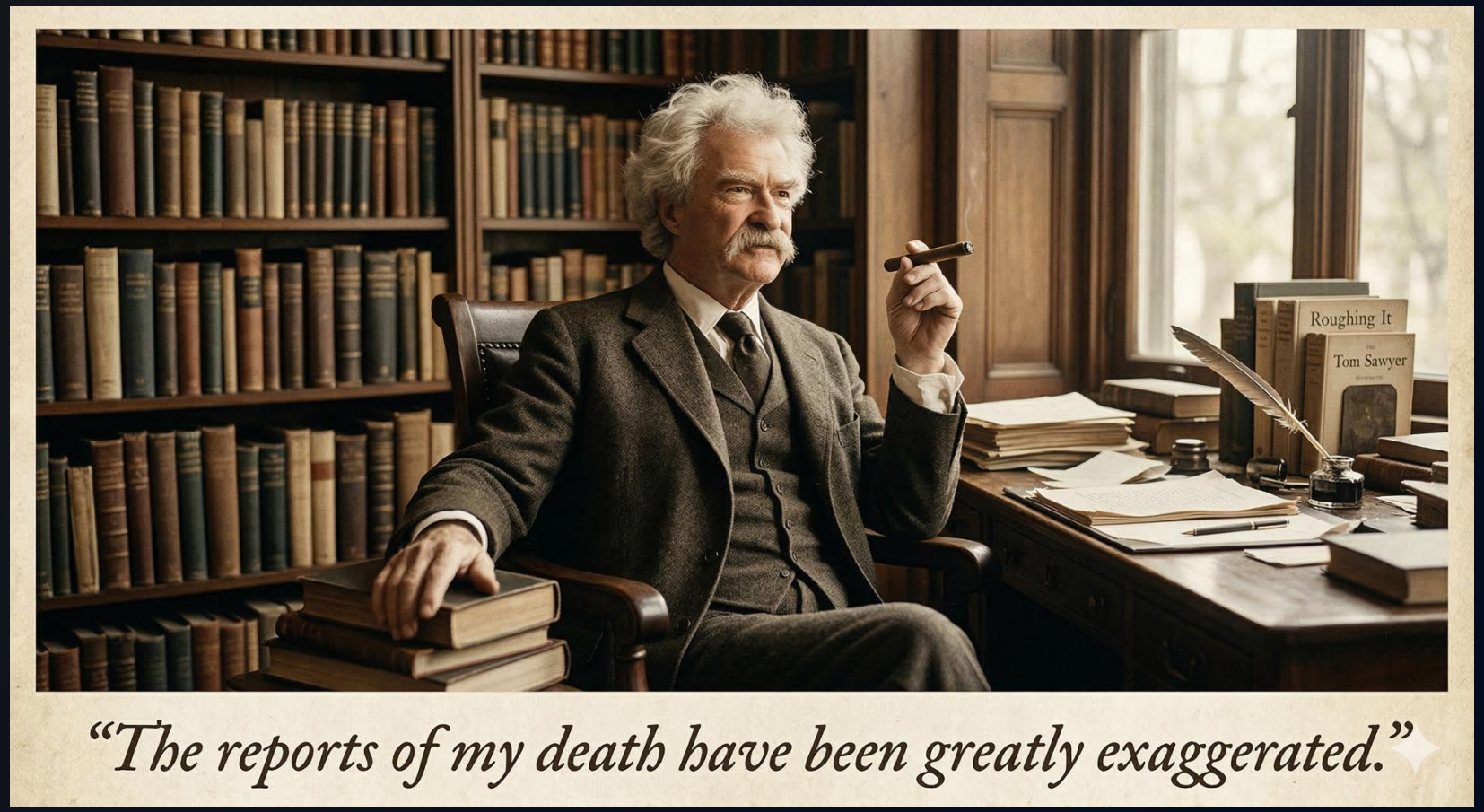
IP

Compute Subsystems (CSS)

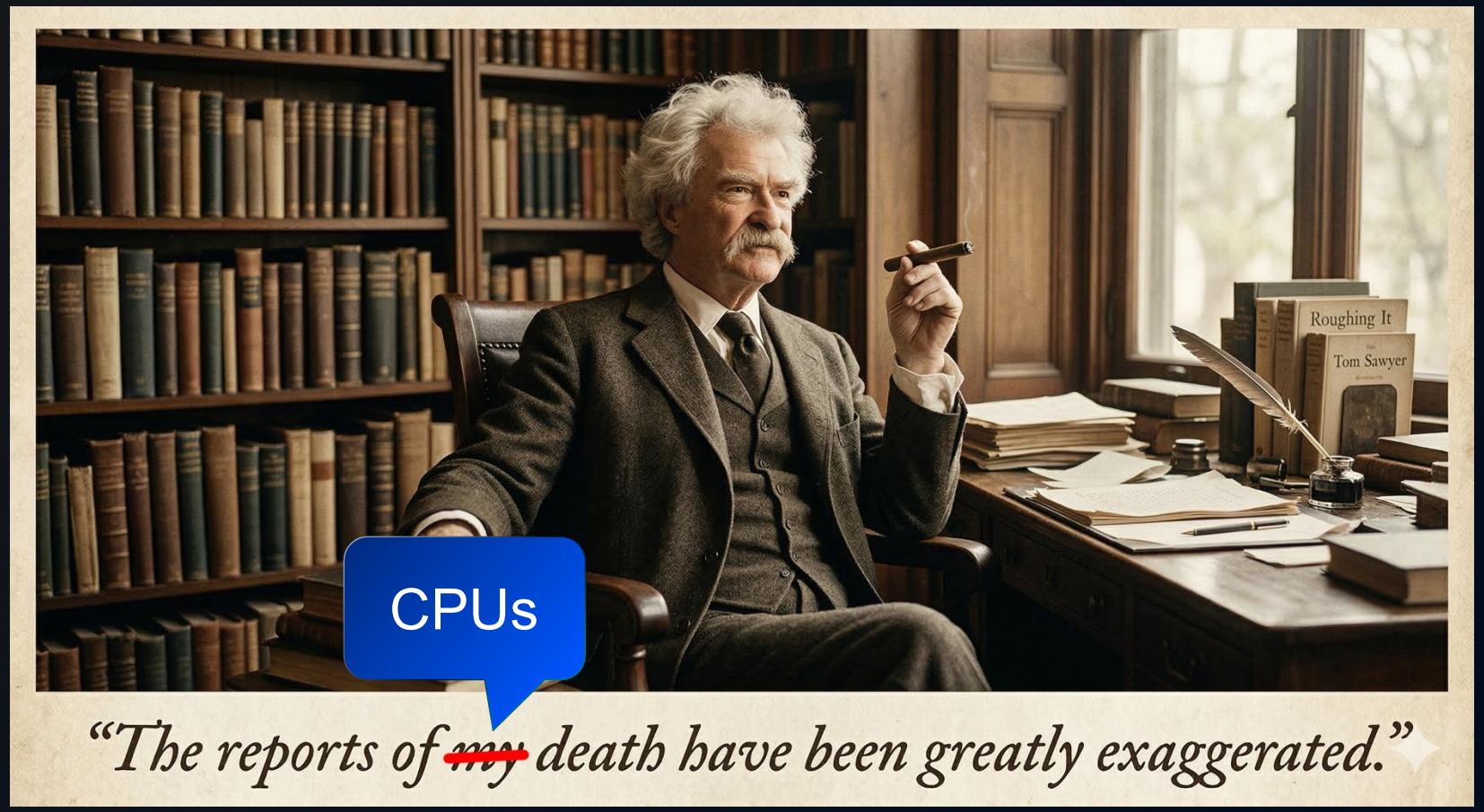
CSS demand exploding



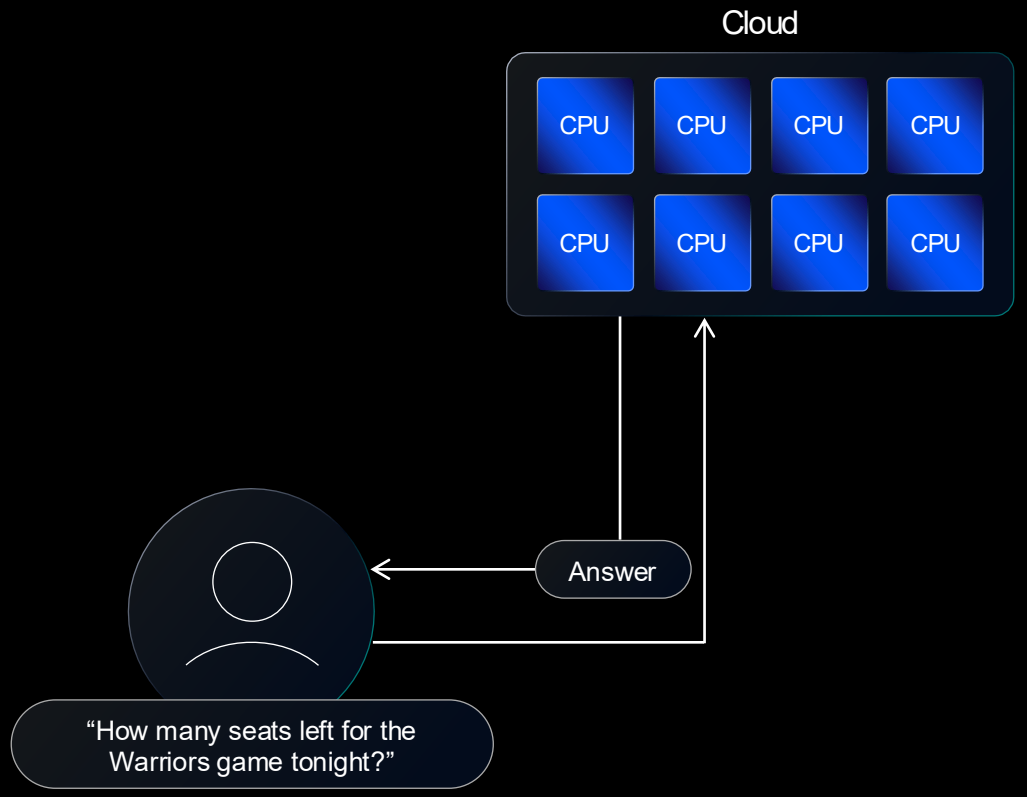
But somewhere along the way…



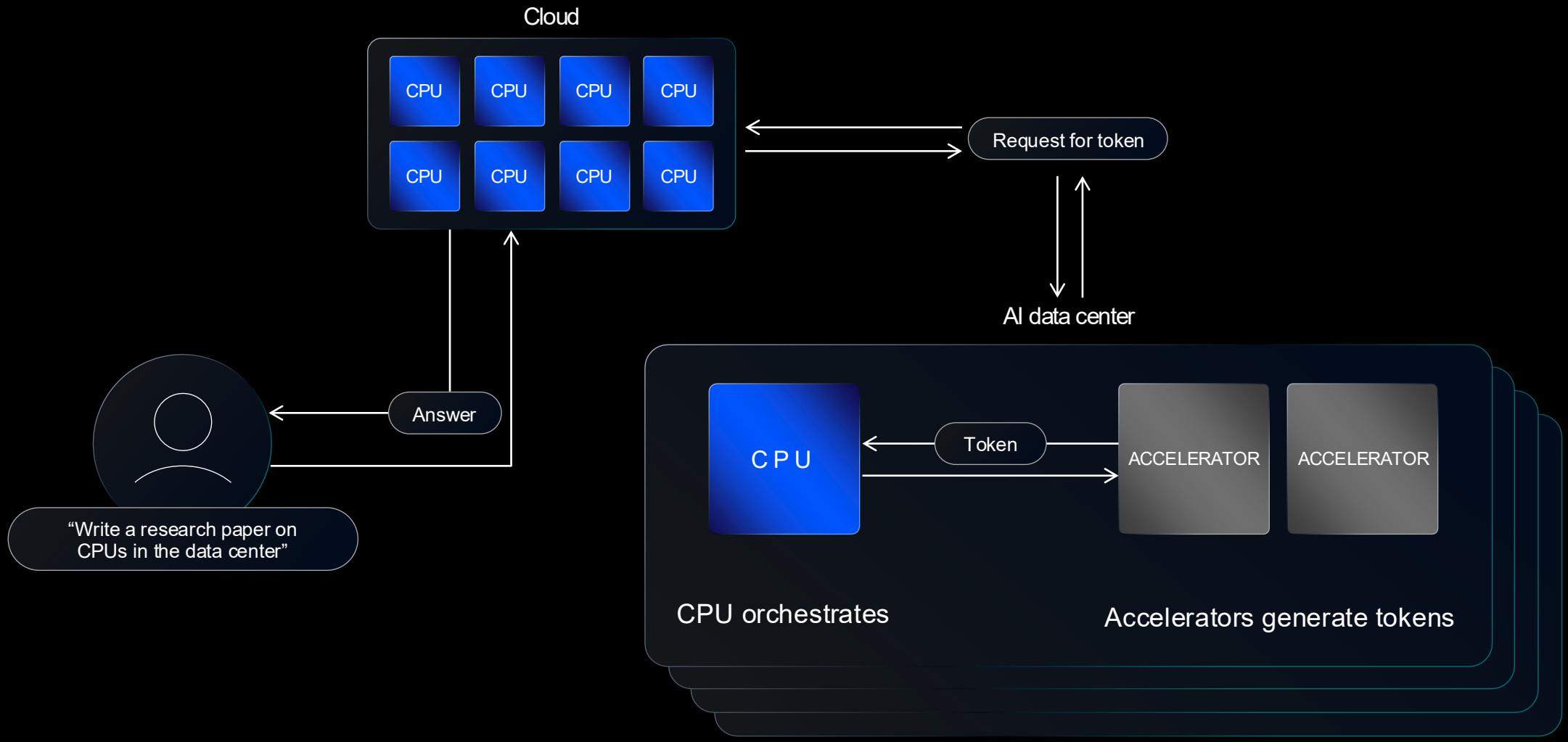
End of an era…?


Roughing It
Tom Sawyer
CPUs
"The reports of ~~my~~ death have been greatly exaggerated."

The cloud before AI



AI cloud - CPUs still doing work!



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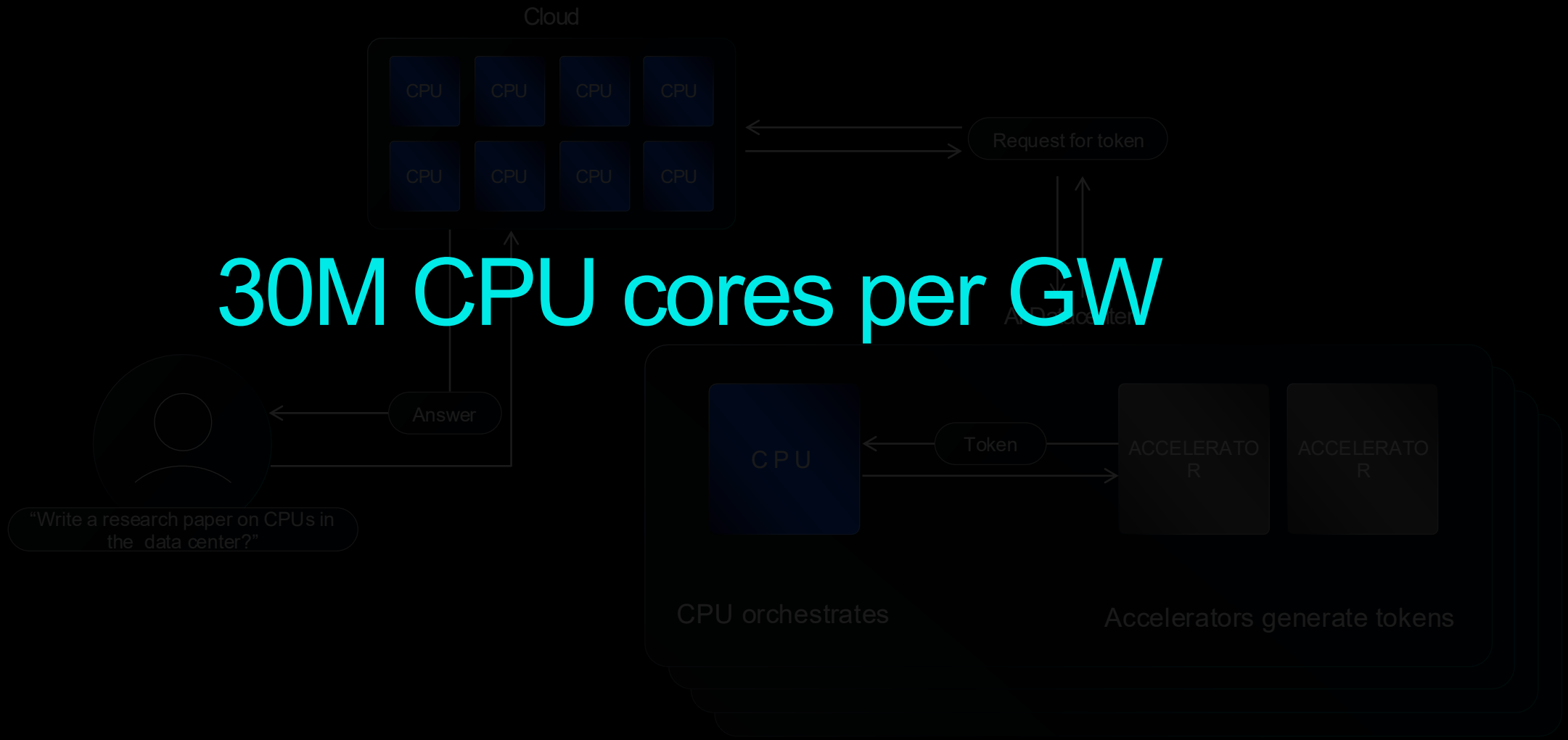
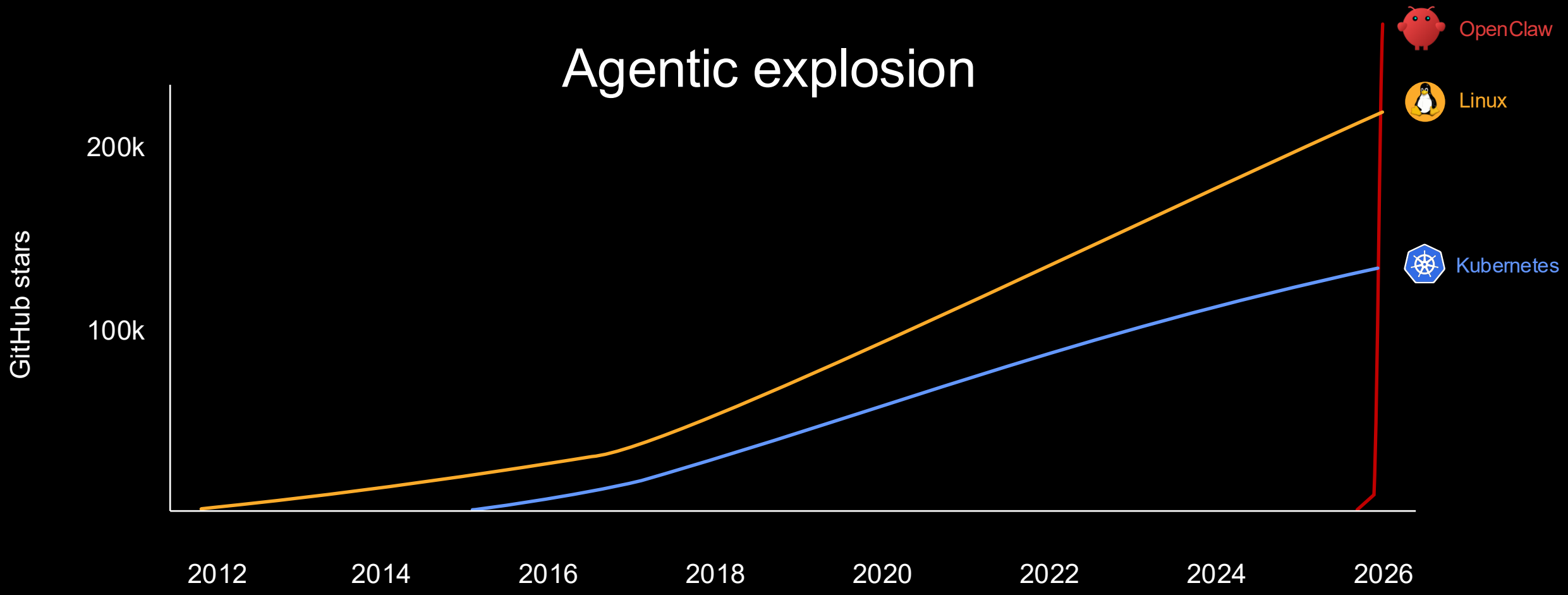
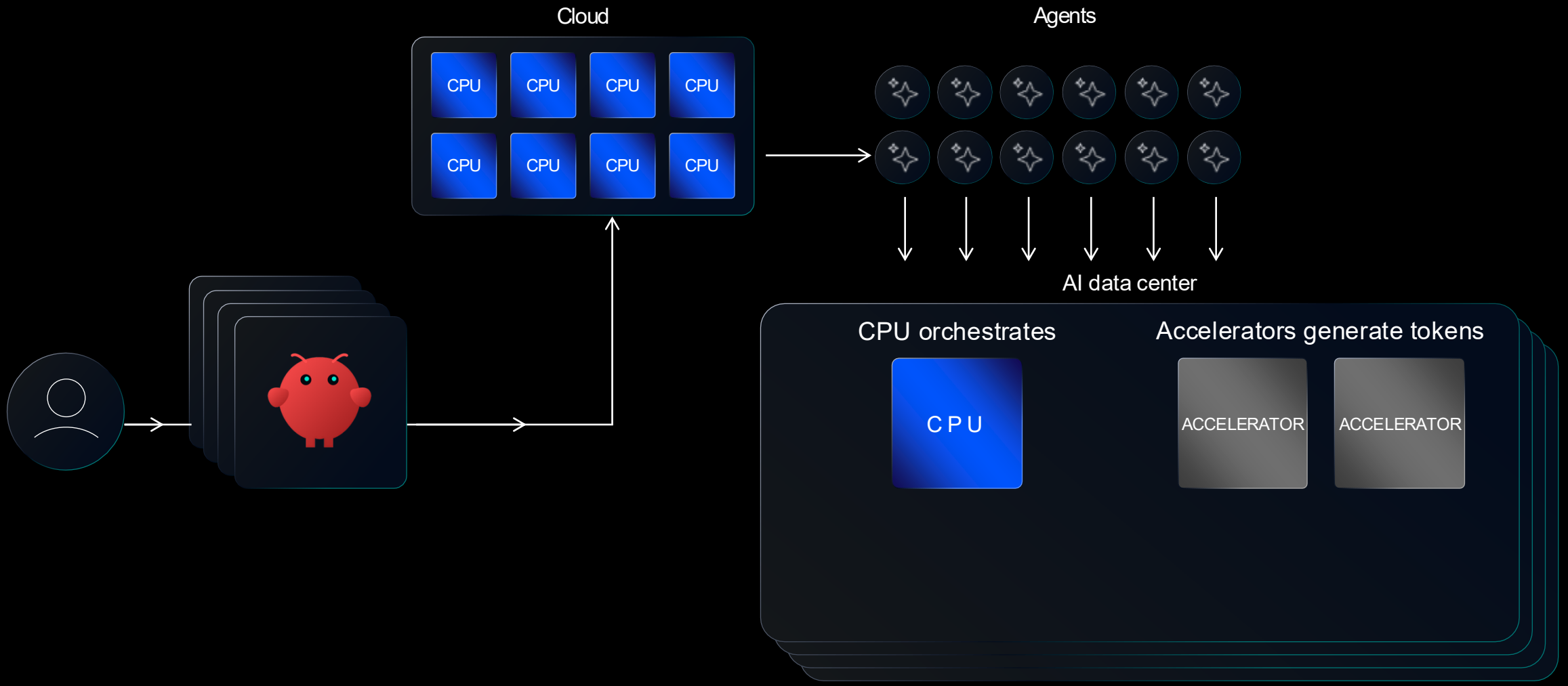


Agentic explosion

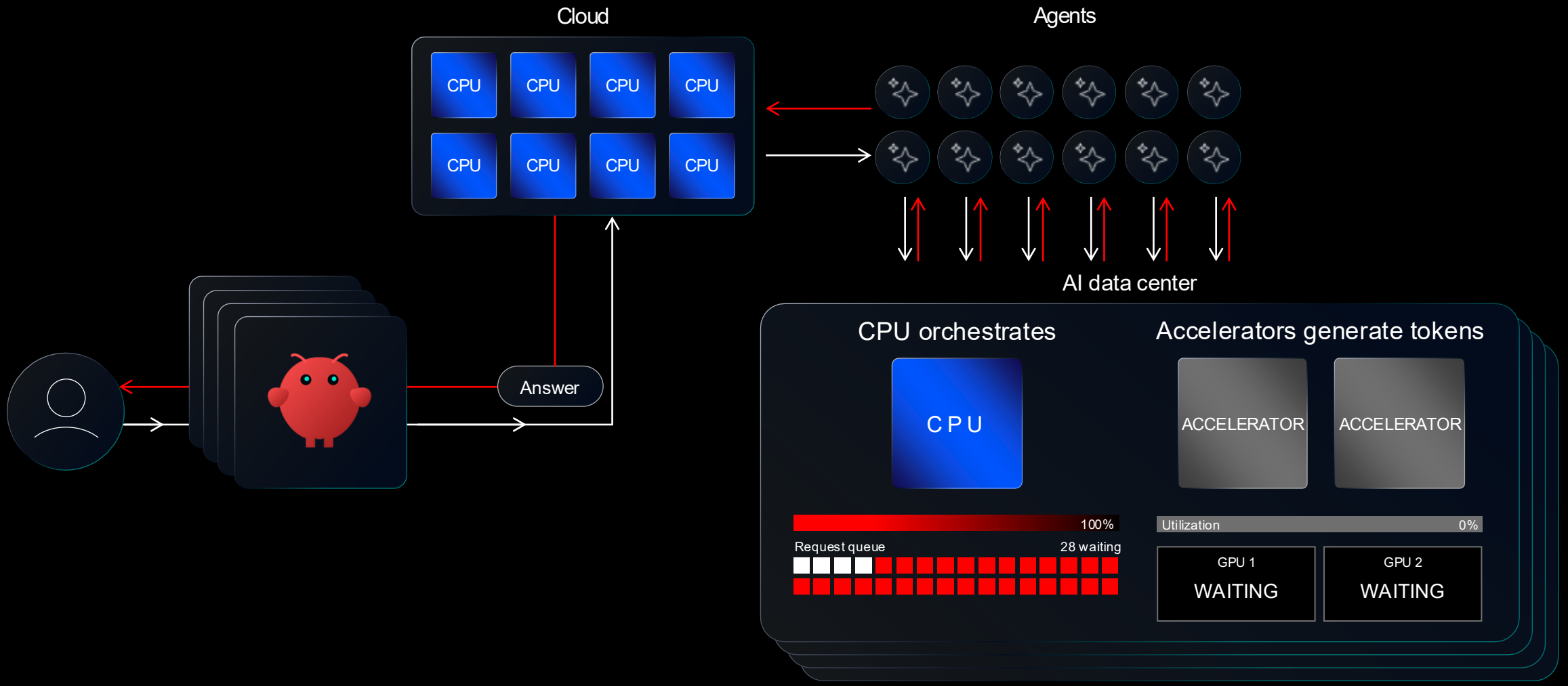


*Based on GitHub adoption trends

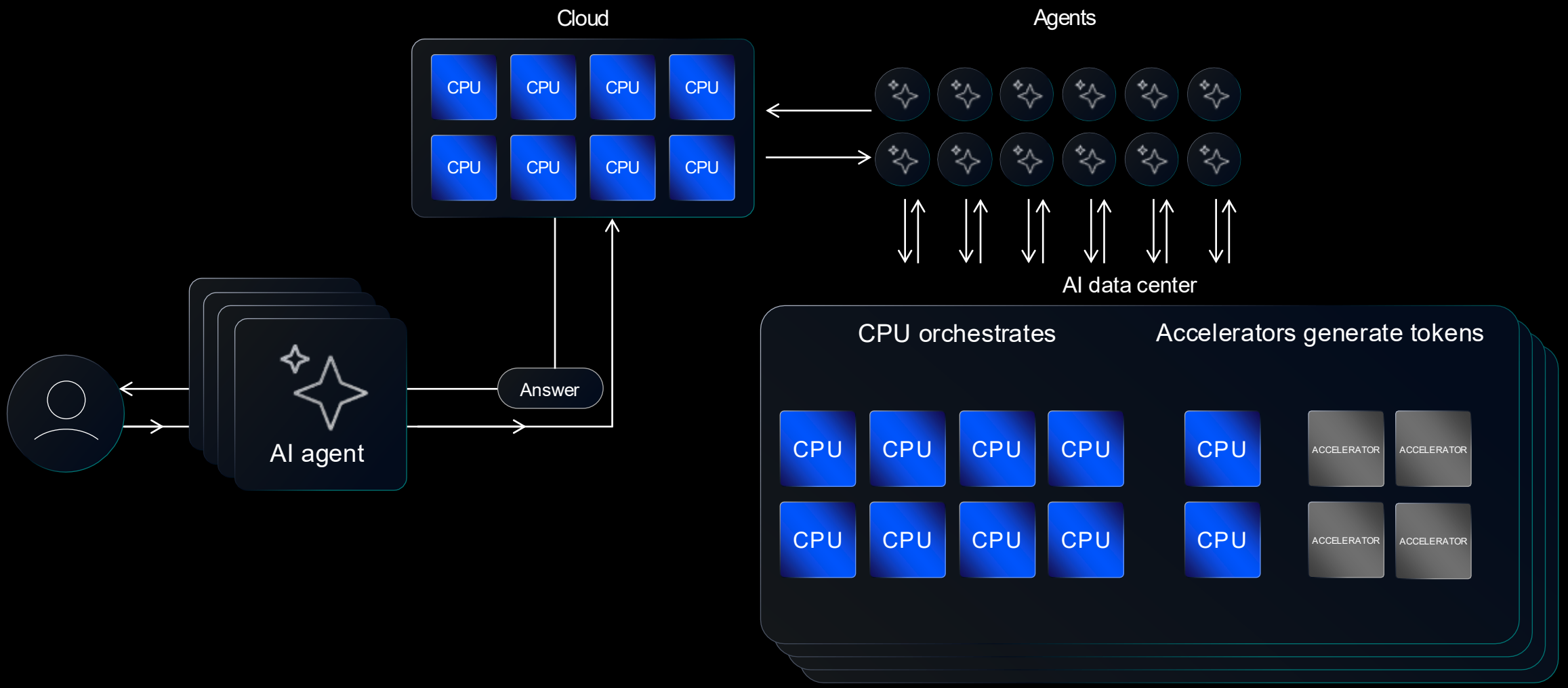
Agents query >15x tokens of humans



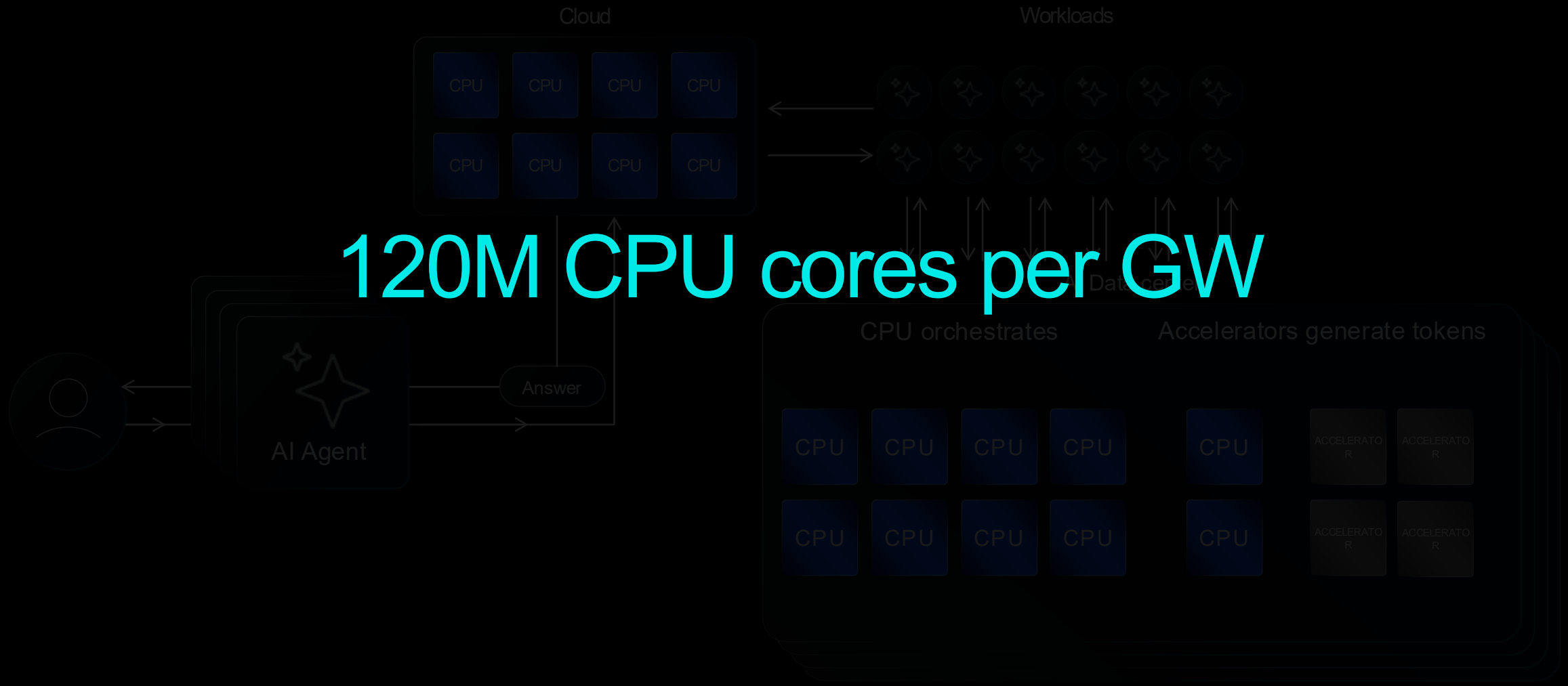
Massive amount of agent workloads swamp CPU



More and more CPUs needed to balance agent flow



More and more CPUs needed



4x CPU cores
in the same power envelope?

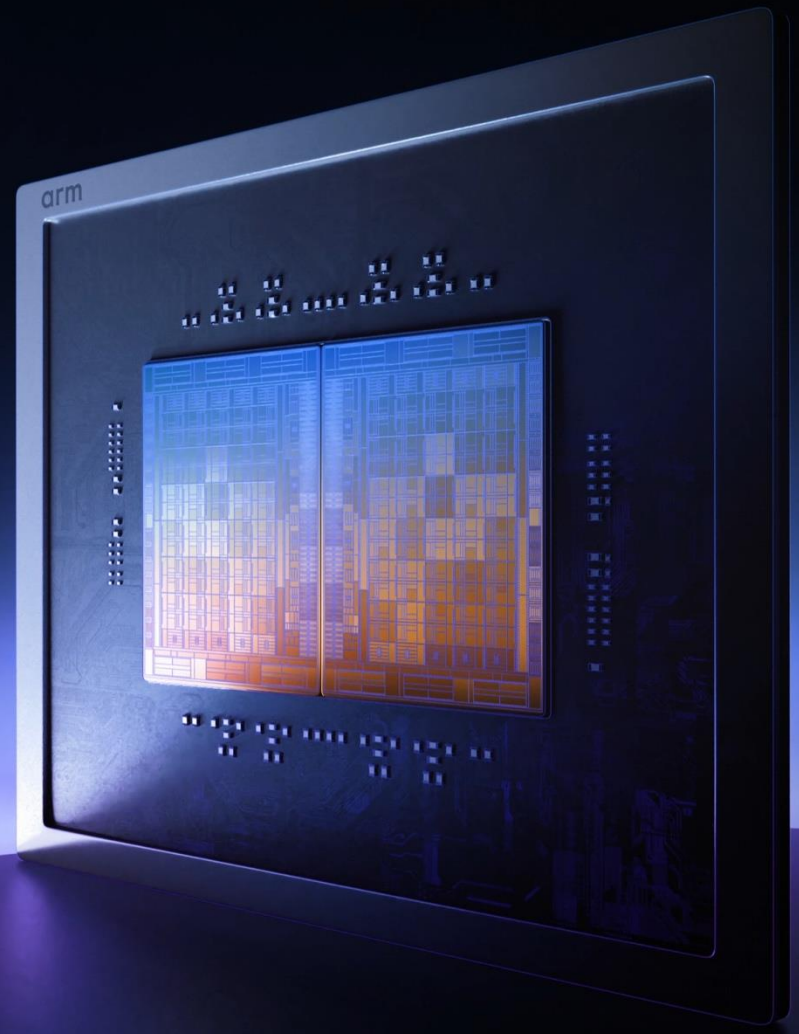
4x CPU cores
in the same power envelope?

That’s a problem

VT

arm AGI CPU

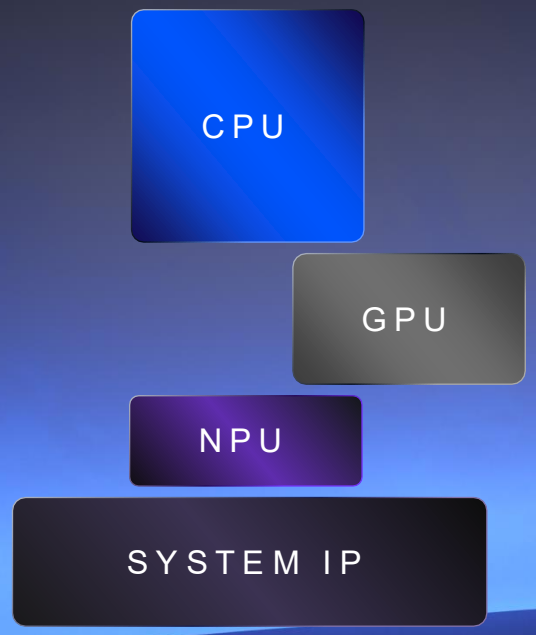
World's most efficient agentic CPU




arm
Meta

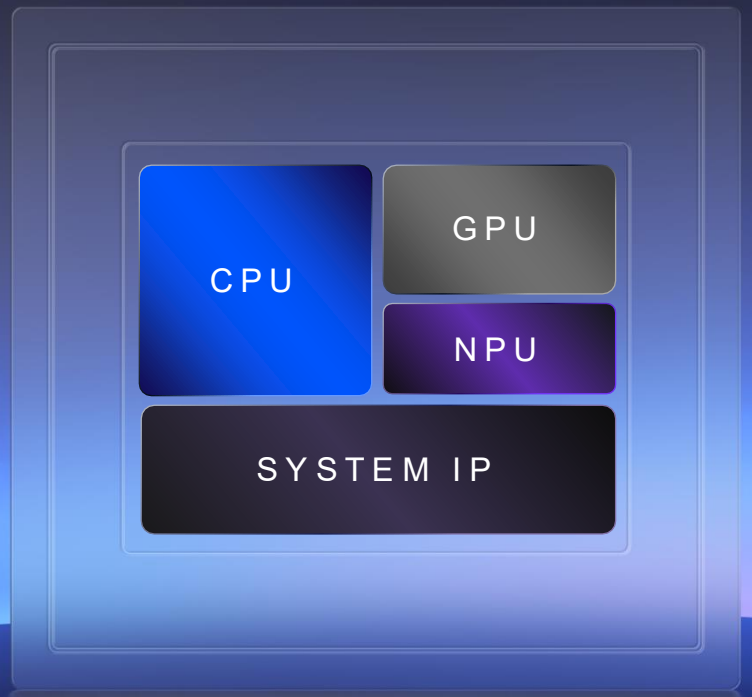
arm | OpenAI

We now deliver



IP

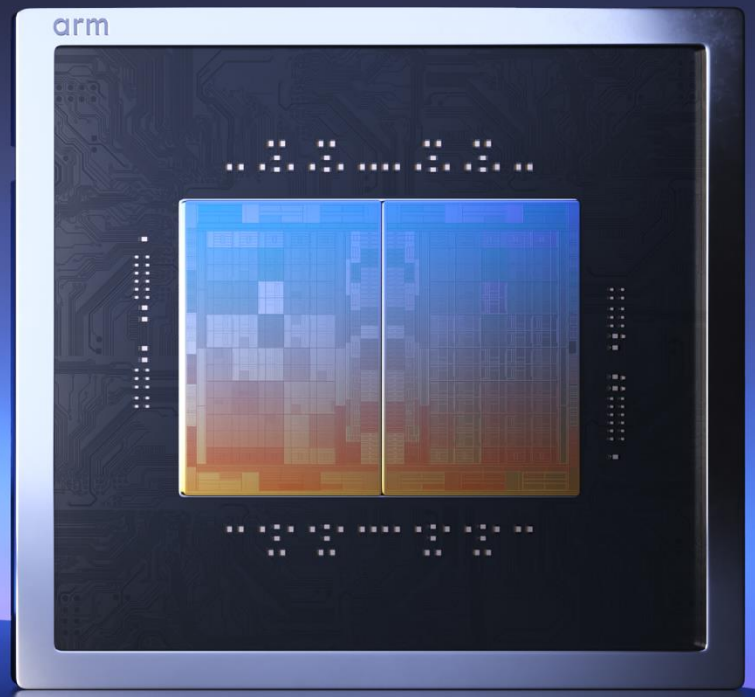
+



Compute Subsystems (CSS)

+

arm AGI CPU



First production silicon chip


aws

BROADCOM

Google Cloud

MARVELL

Microsoft

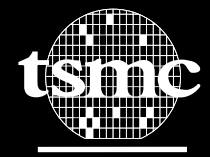
arm

NVIDIA

micron

ORACLE

SAMSUNG

SK hynix

tsmc

arm AGI CPU

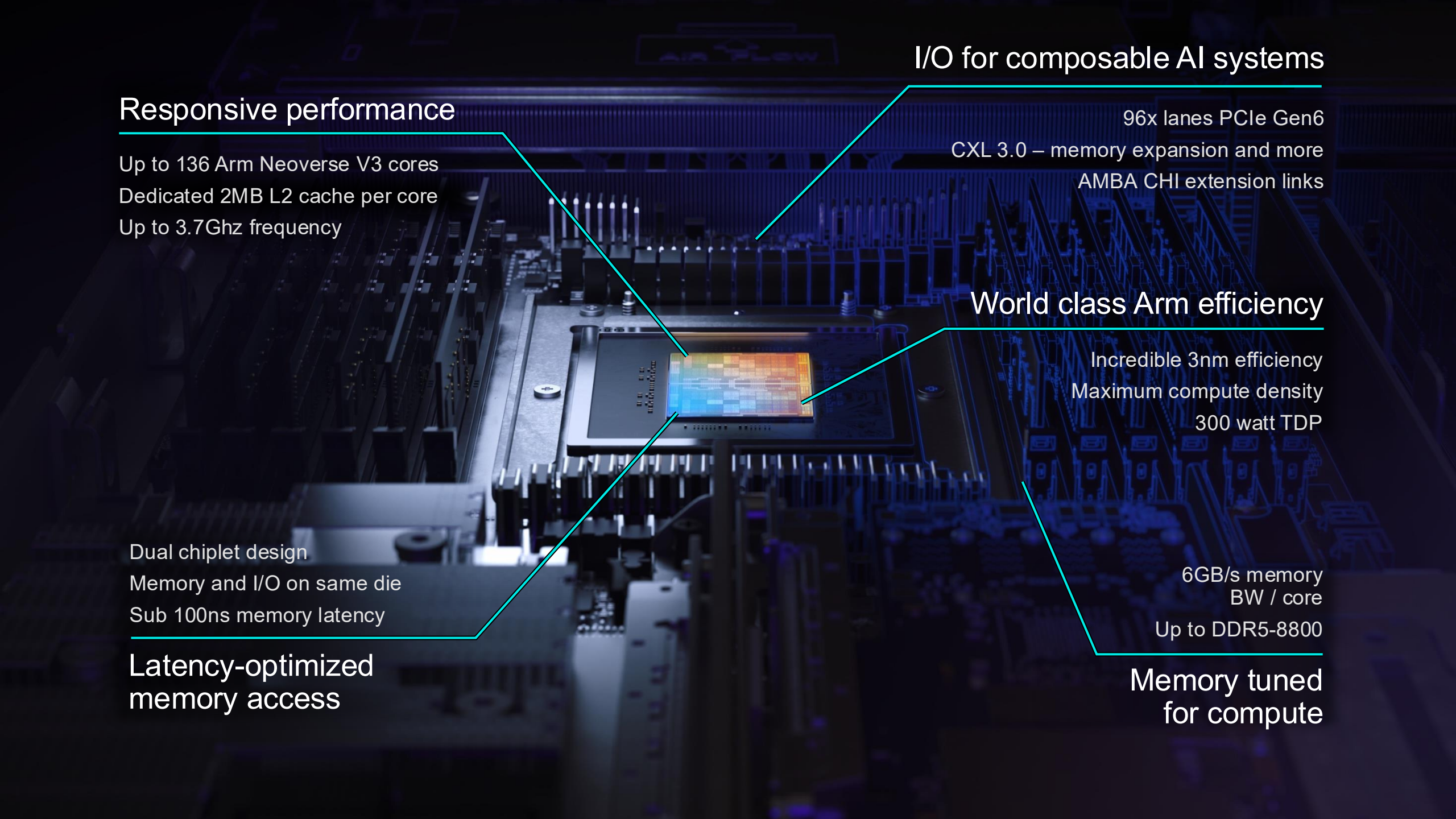
World’s most efficient agentic CPU

Performance

Scale

Efficiency

VT



Responsive performance

Up to 136 Arm Neoverse V3 cores
Dedicated 2MB L2 cache per core
Up to 3.7Ghz frequency

I/O for composable AI systems

96x lanes PCIe Gen6
CXL 3.0 – memory expansion and more
AMBA CHI extension links

World class Arm efficiency

Incredible 3nm efficiency
Maximum compute density
300 watt TDP

Latency-optimized memory access

Dual chiplet design
Memory and I/O on same die
Sub 100ns memory latency

Memory tuned for compute

6GB/s memory BW / core
Up to DDR5-8800

Air cooled

36kW open rack V3

30x 2 node 1U servers

8,160 performant CPU cores

180TB+ low latency memory

Liquid cooled

200kW open rack wide

42x 8 node 1U servers

45,696 performant CPU cores

1PB+ low latency memory

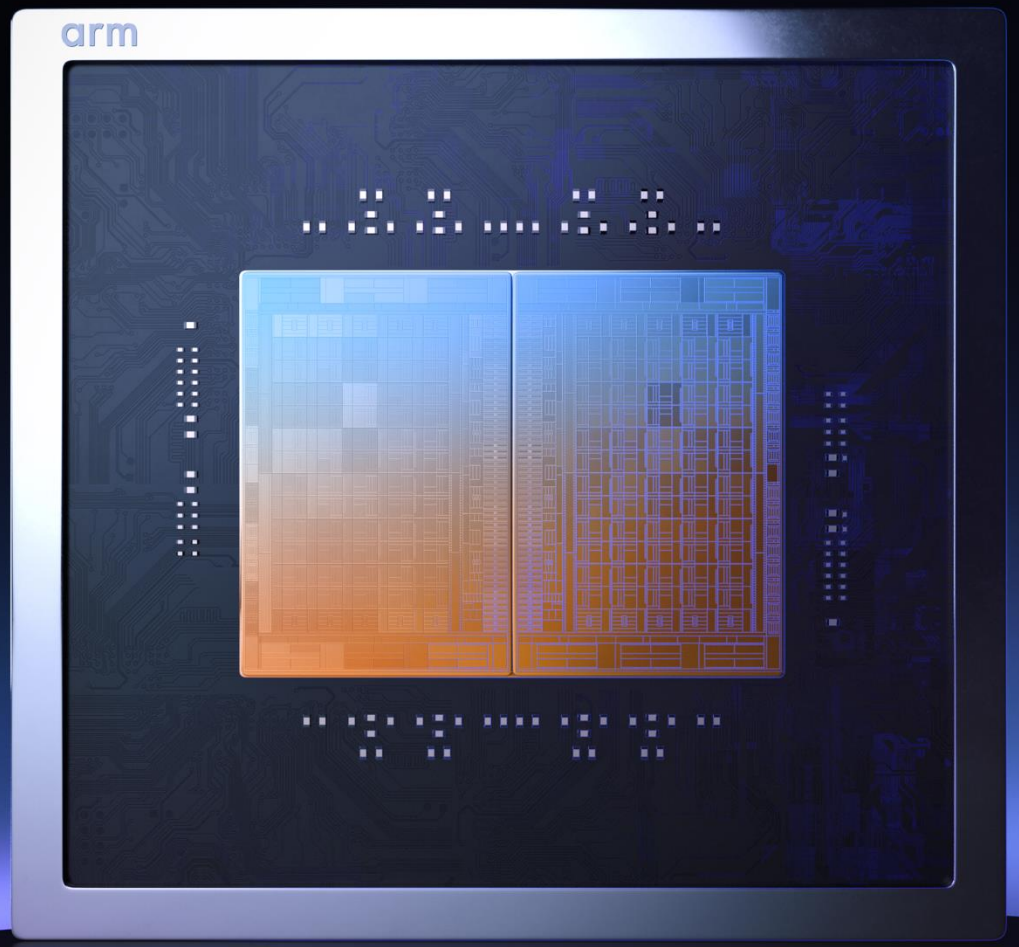
Maximum performance, density & efficiency

OPEN Compute Project®

Contributions across the full stack

- Platform foundation
- Security & access
- Validation & tooling

arm AGI CPU



ASRock Rack

Lenovo

tsmc

Amkor Technology

Quanta Computer

SUPERMICRO

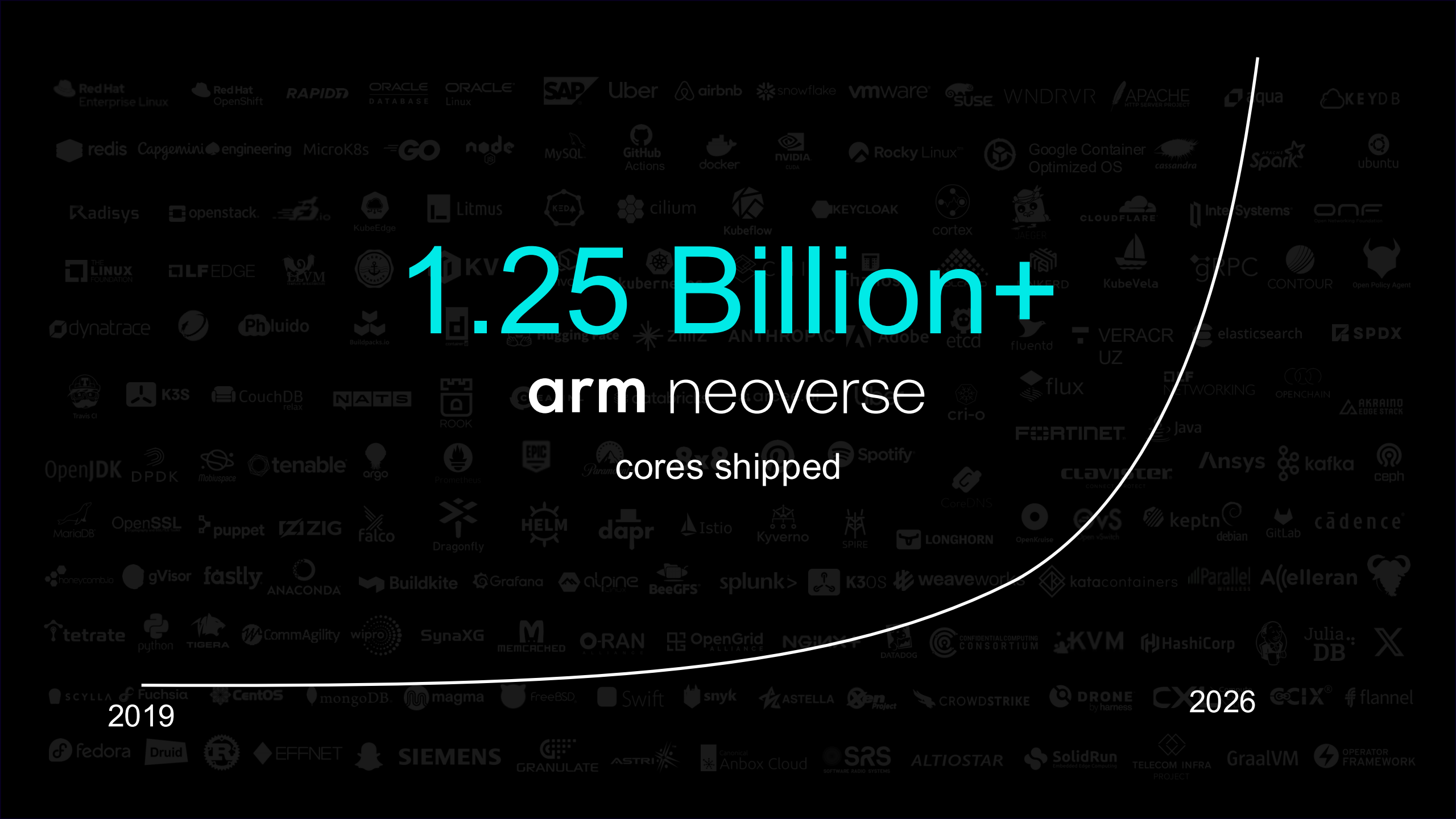
SAMSUNG

micron

SK hynix

Available now

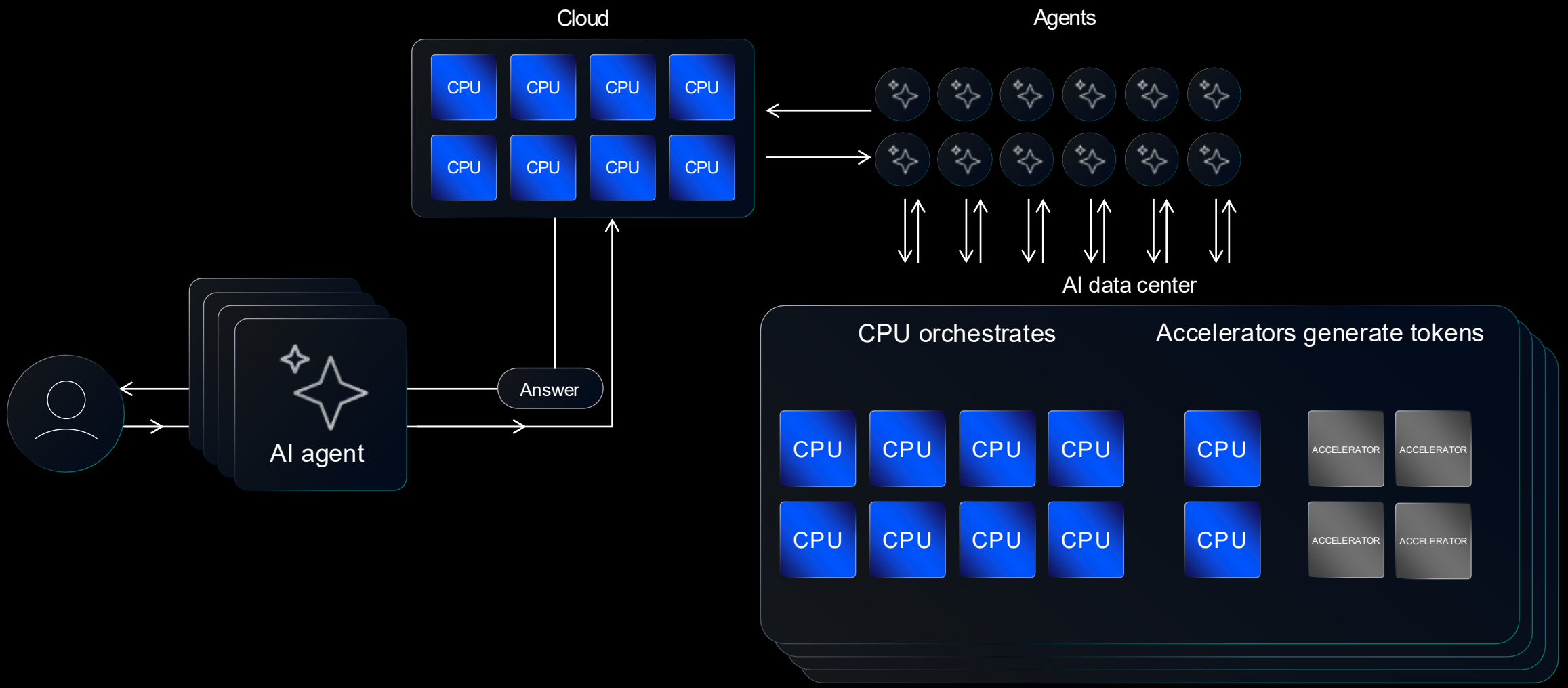
Arm Neoverse compute platform


1.25 Billion+
arm neoverse
cores shipped
2019
2026

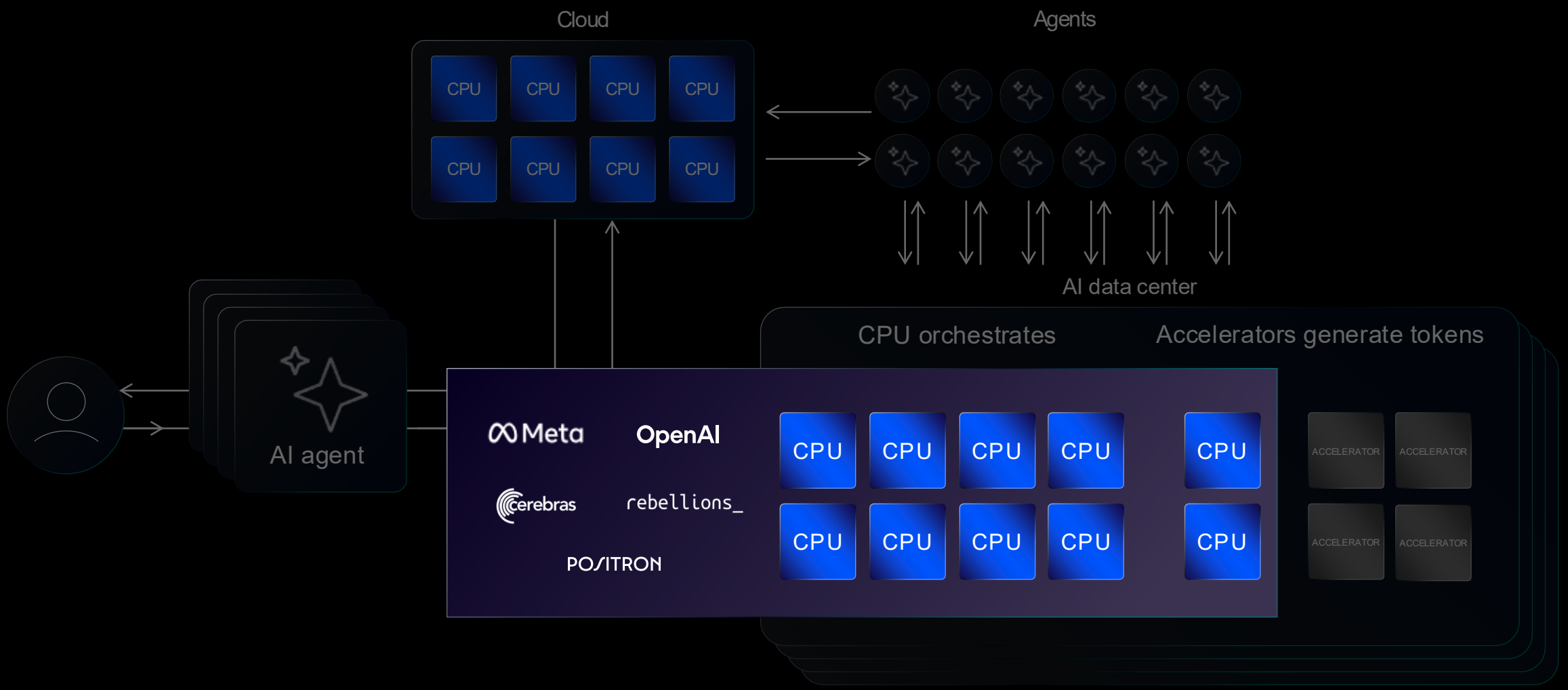
Paul Saab

Meta

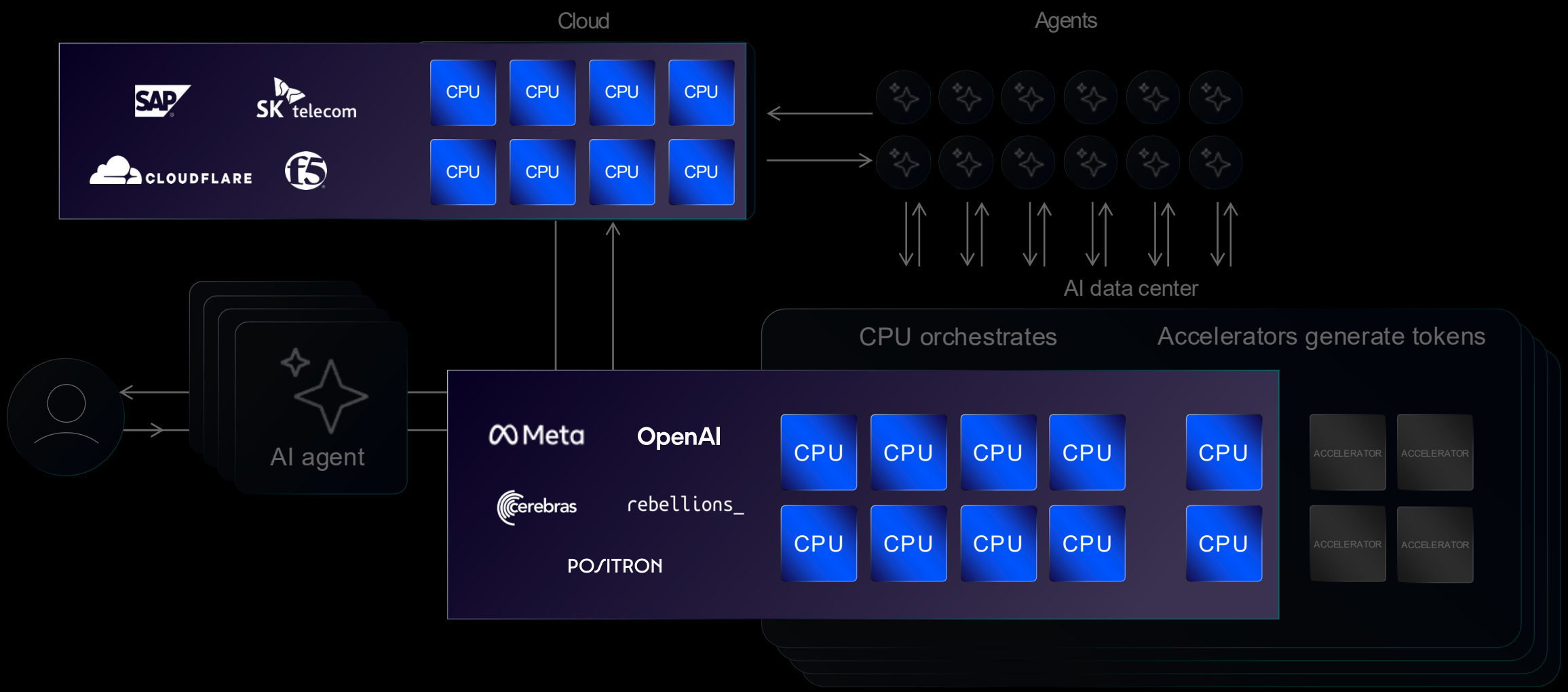
The agentic data center



The agentic data center



The agentic data center




cerebras


CLOUDFLARE


f5


Meta


arm


OpenAI

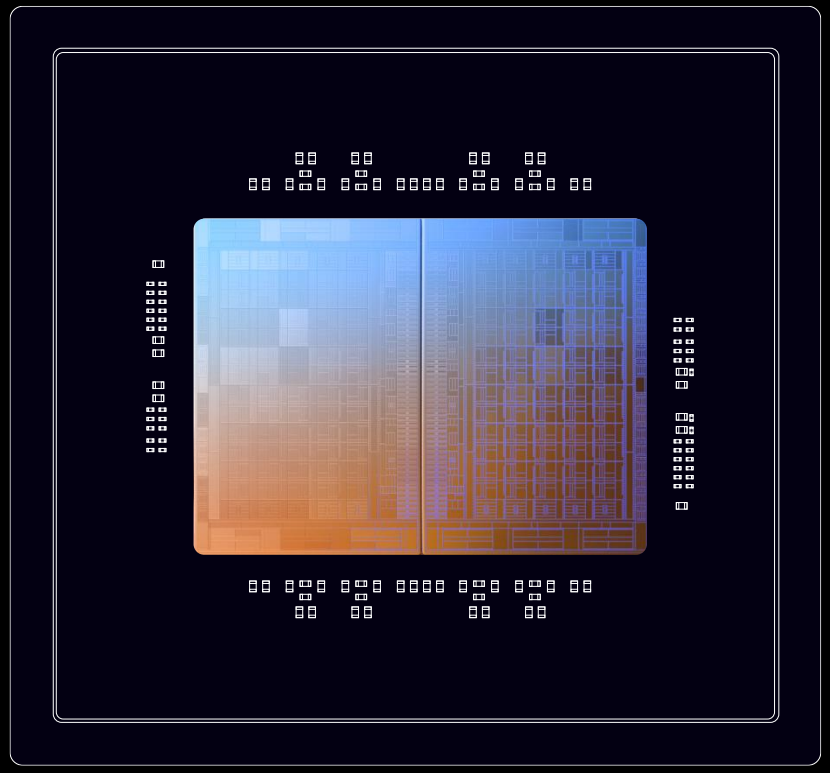

POSITRON


rebellions_


SAP


SK telecom

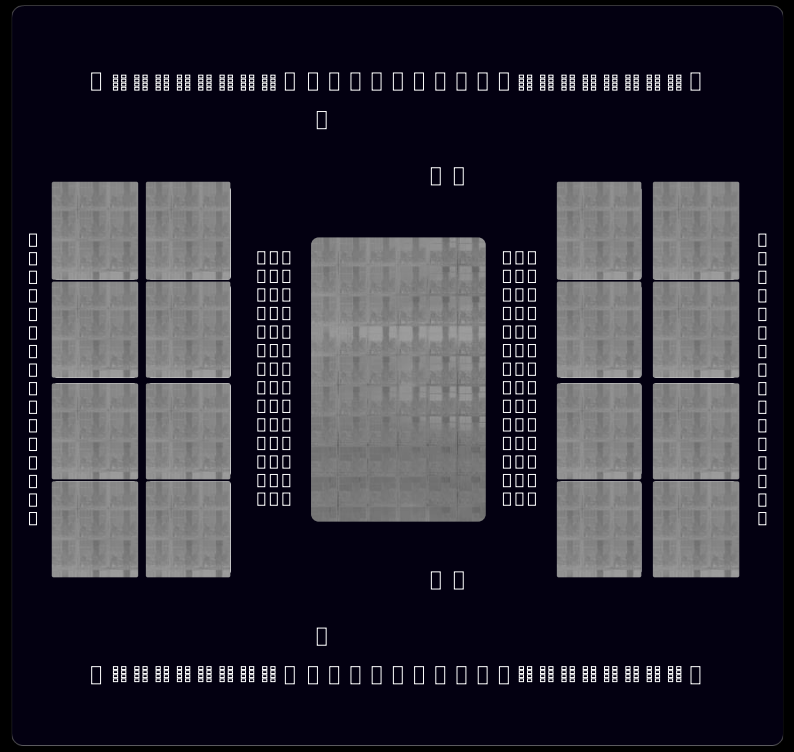
arm AGI CPU



High performance cores
World class efficiency
Low latency design

Performance scales. Power stays predictable.

x86 CPU



Execution overhead
Legacy feature support
Modularity over latency

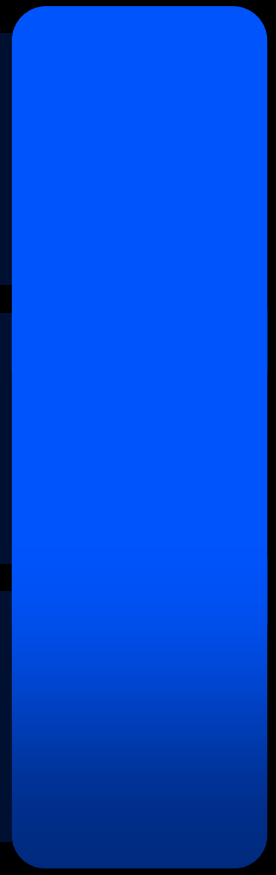
Performance throttled. Technical debt.

Optimizing performance, scale, and efficiency

Efficiency
Purpose-built, no legacy overhead
No wasted energy or silicon

Scale
Linear scaling across cores
Memory and I/O keep pace

Performance
More work per cycle
Full performance, always

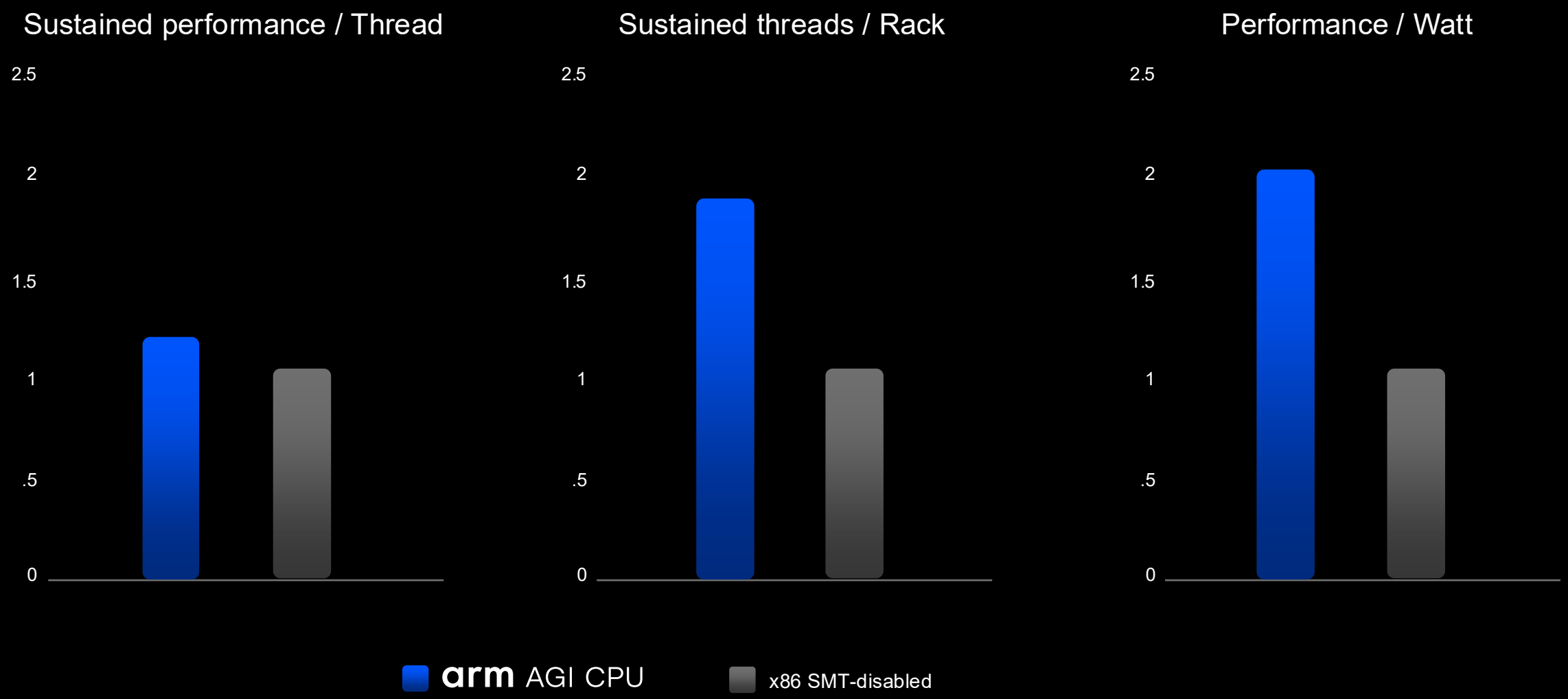


arm AGI CPU

x86 CPU

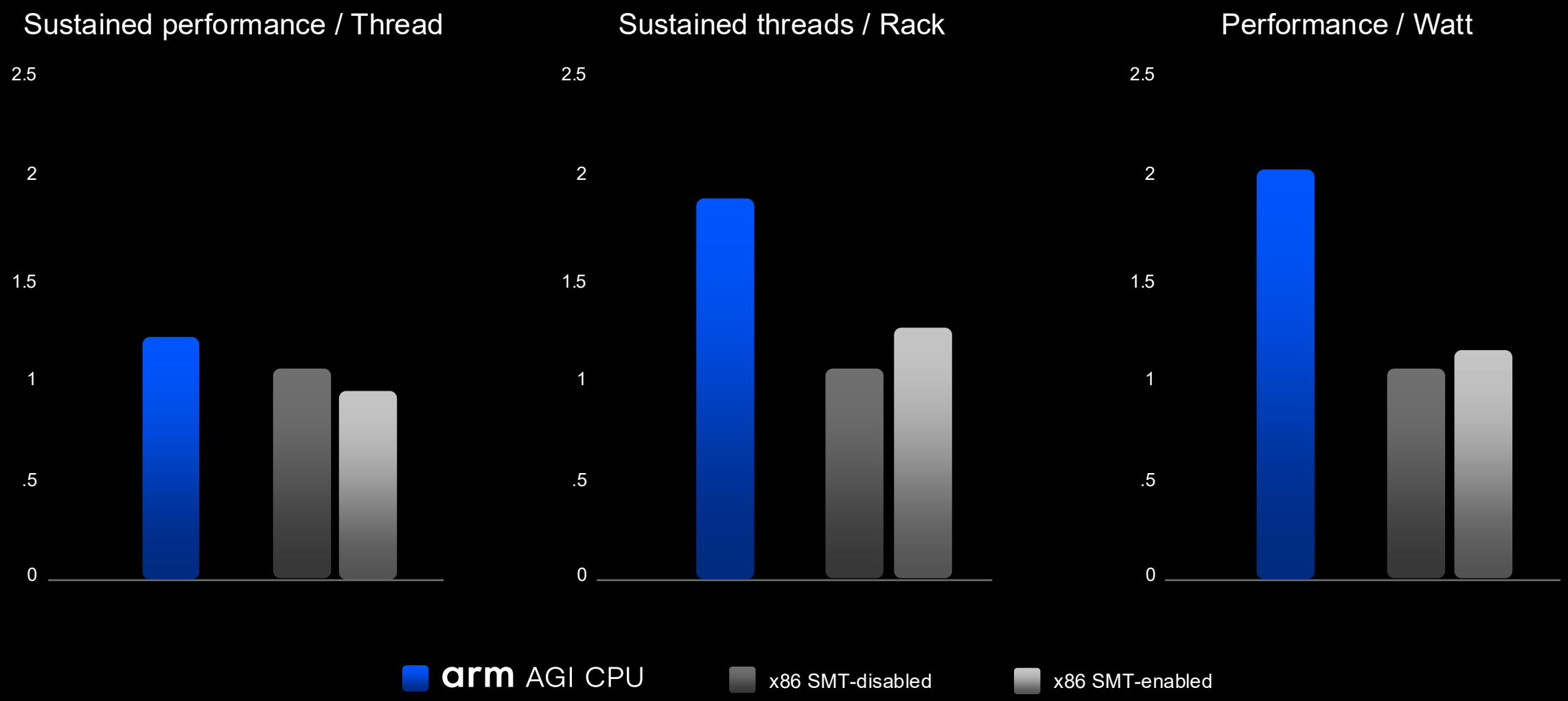
Based on estimates.

Performance. Scale. Efficiency.



Based on estimates.

Performance. Scale. Efficiency.



Based on estimates.

arm AGI CPU

World’s most efficient agentic CPU

Performance

Scale

Efficiency

More than 2x performance per rack on Arm

Arm AGI CPU

36kW rack

30x 1U servers

8,160 CPU cores

VS x86 CPU

36kW rack

17x 2U servers

4,352 CPU cores

Based on estimates.

More than 2x performance per rack delivers

Up to

$10B

CAPEX savings

vs x86 CPU

1GW capacity

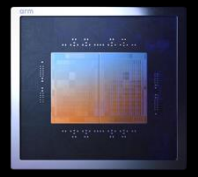
Based on estimates.

Committed to future products

Silicon

Neoverse IP + CSS

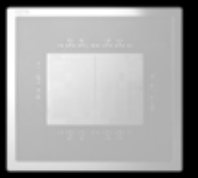
arm AGI CPU

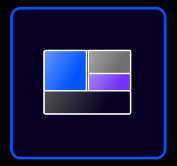


Best in class performance, scale & efficiency

arm AGI CPU 2

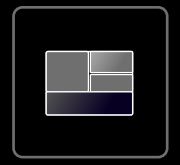
arm AGI CPU 3





CSS V3

CSS V4



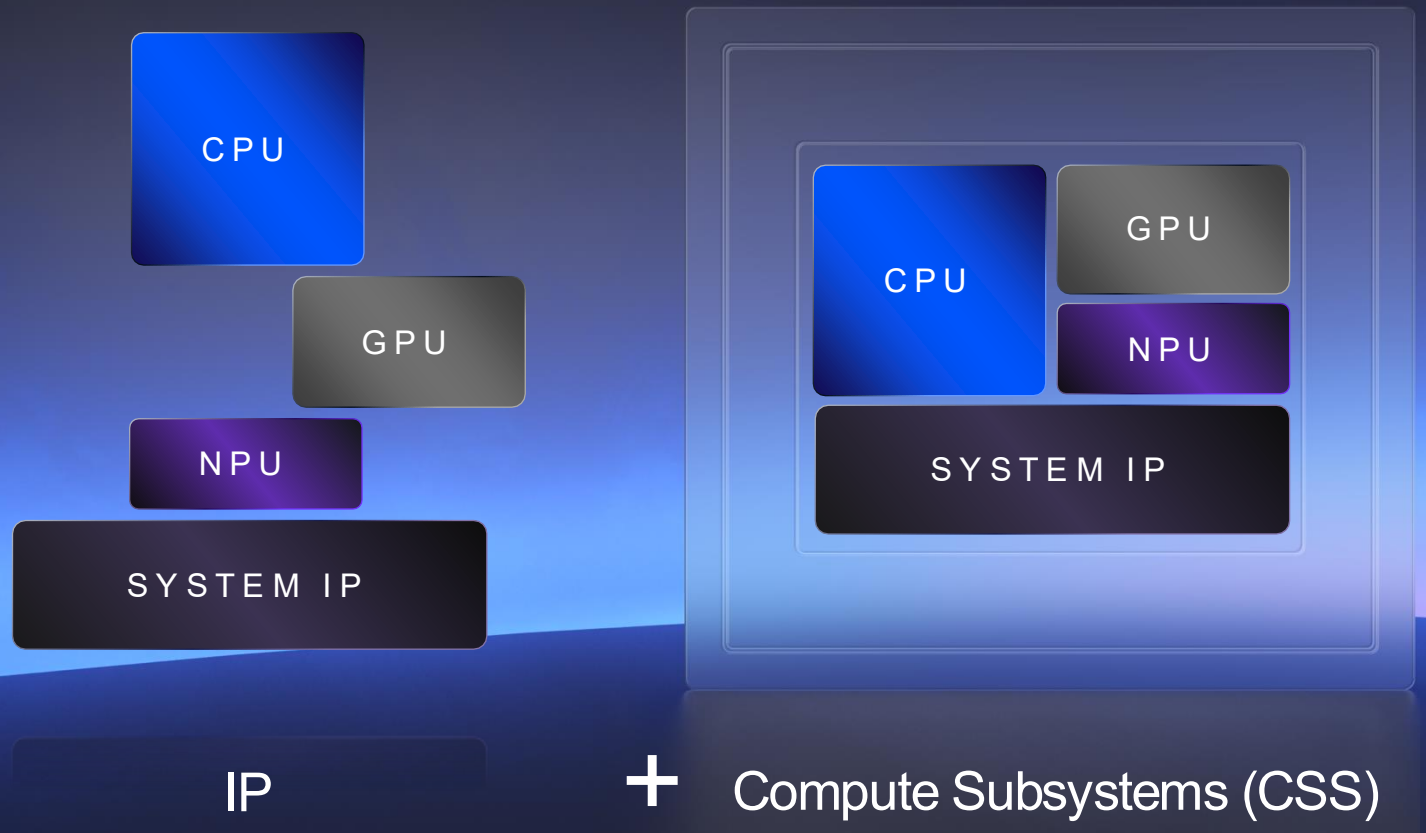
CSS V5

Now

2027

Future

Timelines depict silicon sample availability.



IP + Compute Subsystems (CSS)

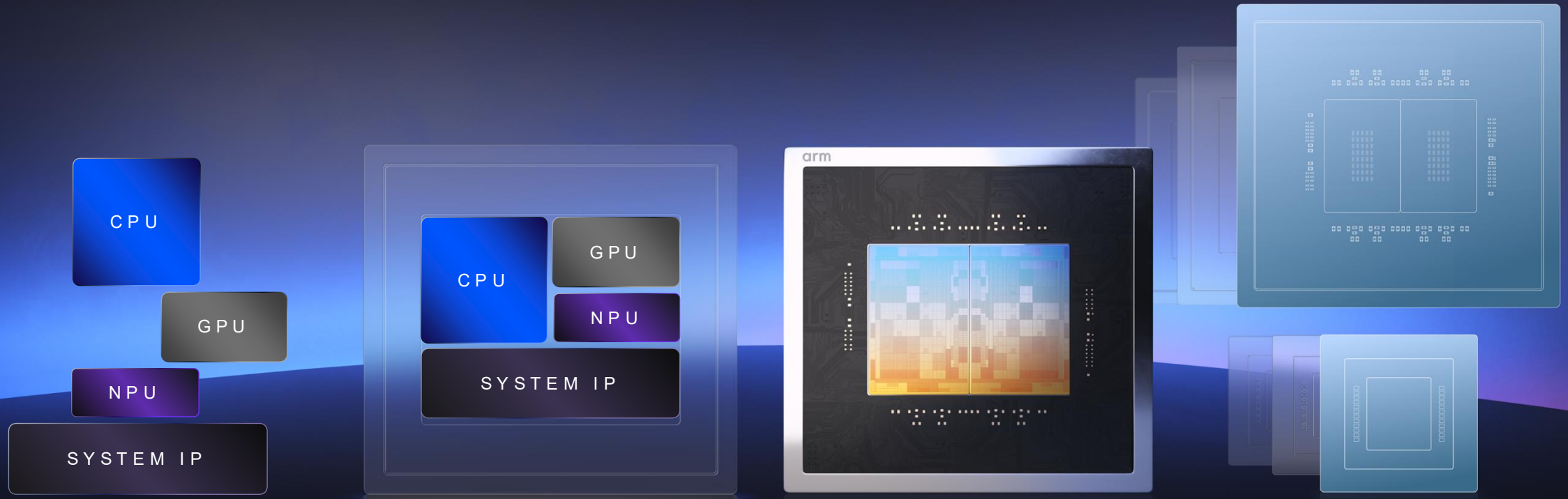
~$3B TAM

IP + Compute Subsystems (CSS)

Based on estimates.


arm AGI CPU
CPU
GPU
NPU
SYSTEM IP
CPU
GPU
NPU
SYSTEM IP
arm
IP
+
Compute Subsystems (CSS)
+
First production silicon chip

Based on estimates.



IP + Compute Subsystems (CSS) + First production silicon chip

We are not stopping here

>$1T TAM

IP + Compute Subsystems (CSS) + First production silicon chip

By 2030

Based on estimates.


ADVANTEST
altera
ami
Amkor Technology
ARISTA
ASE HOLDINGS
ASRock
aws
BROADCOM
cadence
Canonical
cerebras
CISCO
CLOUDFLARE
databricks
f5
FURIOSA
GitHub
Google
Hugging Face
intel foundry services
Lenovo
MARVELL
MEDIATEK
Meta
micron
Microsoft
MongoDB
NVIDIA
NXP
OpenAI
OPEN Compute Project
ORACLE
POSITRON
Quanta Computer
rebellions_
Red Hat
Redis
SAMSUNG
SAP
SIEMENS
SK telecom
SK hynix
snowflake
socionext
STATSChipPAC
SUPERMICRO
SUSE
SYNOPSYS
tsmc
University of BRISTOL